

DOCKET NO.: 00-BN-055  
CLIENT NO.: STMI01-00055  
CUSTOMER NO. 30425



COPY OF PAPERS  
ORIGINALLY FILED

CP2183  
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : ANTHONY X. JARVIS

U.S. Serial No. : 09/751,377

Filed : December 29, 2000

For : BYPASS CIRCUITRY FOR USE IN A PIPELINED  
PROCESSOR

Group No. : 2183

RECEIVED

Examiner : (Not Assigned Yet)

JUL 19 2002

Commissioner of Patents  
Washington, D. C. 20231

Technology Center 2100

Dear Sir:

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

The undersigned hereby certifies that the following documents:

1. Postcard receipt;
2. Information Disclosure Statement;
3. Form PTO-1449; and,
4. Four (4) References.

relating to the above application, were deposited as "First Class Mail" with the United States Postal Service, addressed to: Commissioner for Patents; Washington, D.C. 20231, on July 11, 2002.

Date: July 11, 2002

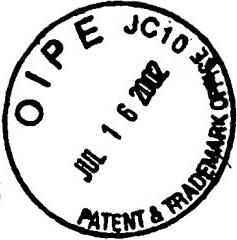
Date: 11 July 2002

John T. Mockler  
Mailer

John T. Mockler  
John T. Mockler  
Reg. No. 39,775

P.O. Box 802432  
Dallas, Texas 75380  
Phone: (214) 922-9221  
Fax: (214) 969-7557  
E-mail: [jmockler@davismunck.com](mailto:jmockler@davismunck.com)

DOCKET NO.: 00-BN-055  
CLIENT NO.: STMI01-00055  
CUSTOMER NO. 30425



COPY OF PAPERS  
ORIGINALLY FILED

PATENT

8/25/02  
JMM

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : ANTHONY X. JARVIS  
U.S. Serial No. : 09/751,377 ✓  
Filed : December 29, 2000  
For : BYPASS CIRCUITRY FOR USE IN A PIPELINED PROCESSOR  
Group No. : 2183  
Examiner : (Not Assigned Yet)

RECEIVED

JUL 19 2002

Technology Center 2100

Commissioner of Patents  
Washington, D. C. 20231

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement.

This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure. The patents, publications and other information herein are listed below and on the attached Form PTO-1449. Copies of the listed references are submitted herewith.

U.S. Patents

<u>Patent No.</u>	<u>Inventor</u>	<u>Date</u>
5,872,986	Heeb	Feb. 16, 1999
6,078,196	Gayles	Jun. 20, 2000

RECEIVED

JUL 19 2002

DOCKET NO. 00-BN-055  
U.S. SERIAL NO. 09/751,377

Technology Center 2100

PATENT

Foreign Patents

Patent No.

EP 0 730 223 A1

Country

EPO

Date

Sep. 4, 1996

Publications

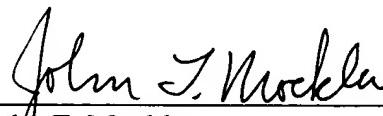
TRAINIS; Simon A.; "MODELLING THE HARDWARE COST OF FULL REGISTER BYPASSING IN A MULTIPLE INSTRUCTION ISSUE PROCESSOR"; XP 000679755;, Journal of Systems Architecture; 1997 Published by Elsevier Science B.V.; Pgs. 39-46

Applicant hereby expressly reserves the right to swear behind the effective dates of any of the above Patents and to question the relevance and materiality of the Patents and Publications listed herein, in whole, in part, or in combination, subsequent to filing this Information Disclosure Statement.

Respectfully submitted,

DAVIS MUNCK, P.C.

Date: 11 July 2002

  
John T. Mockler  
Registration No. 39,775

P.O. Box 802432  
Dallas, Texas 75380  
Telephone: (214) 922-9221  
Facsimile: (214) 969-7557  
Email: [jmockler@davismunck.com](mailto:jmockler@davismunck.com)